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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/007,836	11/05/2001	William C. Moyer	SC90674A CD1	8532
23125	7590	10/05/2004	EXAMINER	
FREESCALE SEMICONDUCTOR, INC.			DONAGHUE, LARRY D	
LAW DEPARTMENT			ART UNIT	PAPER NUMBER
7700 WEST PARMER LANE MD:TX32/PL02				
AUSTIN, TX 78729			2154	

DATE MAILED: 10/05/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	10/007,836	MOYER ET AL.
Examiner	Art Unit	
Larry D Donaghue	2154	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 01 July 2004.
 2a) This action is FINAL. 2b) This action is non-final.
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 33-46 is/are pending in the application.
 4a) Of the above claim(s) 33-37 is/are withdrawn from consideration.
 5) Claim(s) _____ is/are allowed.
 6) Claim(s) 38-46 is/are rejected.
 7) Claim(s) _____ is/are objected to.
 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.
 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) Notice of References Cited (PTO-892)
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
 Paper No(s)/Mail Date 02/25/2003.

4) Interview Summary (PTO-413)
 Paper No(s)/Mail Date. _____.
 5) Notice of Informal Patent Application (PTO-152)
 6) Other: _____.

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1. Claims 33-46 are presented for examination.
2. Applicant's election with traverse of restriction in the reply filed on 07/01/2004 is acknowledged. The traversal is on the ground(s) that the would be no burden. This is not found persuasive for the reason set forth in the restriction requirement in the last office action

The requirement is still deemed proper and is therefore made FINAL.

3. Claims 33-37 are withdrawn from consideration.
4. Applicant is to update the status of all co-pending and related application cited in the specifications.
5. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

6. Claim 44 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.
7. Claim 44 recites the limitation the third control signal. There is insufficient antecedent basis for this limitation in the claim.

8. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

9. Claims 38-46 are rejected under 35 U.S.C. 102(b) as being clearly anticipated by Lee et al. (4,763,242).

Lee et al. taught the invention (claim 38) as claimed including a processor to initiate, via a coprocessor bus (159, col. 2, lines 57-63), execution by a coprocessor of an instruction received by the processor for execution thereby (col. 2, line 29-col. 3, line 42), the method comprising: receiving said instruction (col. 3, lines 10-16); decoding said instruction; providing to said coprocessor, at least partially coincident with said decoding at least a predetermined portion of said instruction via a first portion of said coprocessor bus (159, col. 2, lines 57-63, and col. 4, lines 48-68) and a first control signal indicating that said instruction is being decoded by said processor via a second portion of said coprocessor bus (col. 8, lines 3-8); and receiving from said coprocessor, a second control signal indicating whether said predetermined portion of said instruction caused an exception within said coprocessor (col. 8, lines 57-62).

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As to claim 39, Lee et al. taught the second control signal is received prior to said processor completing said instruction (col. 8, lines 57-62).

As to claim 40, Lee et al. taught discarding said instruction (col. 8, lines 21-40).

As to claim 41, Lee et al. taught negating the first control signal (col. 8, lines 3-8).

As to claim 42, Lee et al. taught providing a third control signal to said coprocessor bus to indicate when execution of said instruction is proceeding, wherein if the first control signal is negated, the third control signal is not asserted (col. 8, lines 3-15).

As to claim 43, Lee et al. taught providing a third control signal to said coprocessor bus to indicate when execution of said instruction is proceeding, wherein if the first control signal is asserted, the third control signal is asserted (col. 8, lines 44-50, lines 58-63).

Claims 44-46 fail to teach or define above or beyond claims 38-44, and are rejected for the reasons set forth supra.

10. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

5,341,482 Cutler et al.

5,953,741 Evoy et al.

5,119,499 Tonomura et al.

5,715,439 Schmidt et al.

5,898,897 Son et al.

5,109,514 Garner et al.

Computer Organization Hamacher et al.

11. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Larry D Donaghue whose telephone number is 703-305-9675. The examiner can normally be reached on M-F 8:00-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John Follansbee can be reached on 703-305-8498. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

LARRY D. DONAGHUE
PRIMARY EXAMINER

